

w_mpi_module_base::
l_nm_darray01

w_mpi_module_base::
l_nm_darray10

w_mpi_module_base::
l_nm_darray11

w_mpi_module_base::
l_nm_darray00

```
graph LR; A[w_mpi_module_base::  
l_nm_darray01] --> C[w_mpi_module_base::  
l_nm_darray00]; B[w_mpi_module_base::  
l_nm_darray10] --> C; D[w_mpi_module_base::  
l_nm_darray11] --> C;
```

The diagram illustrates a flow of information or data from three source boxes on the left to a single target box on the right. The source boxes are labeled with MPI module base identifiers and array names: l_nm_darray01, l_nm_darray10, and l_nm_darray11. The target box is labeled with w_mpi_module_base::l_nm_darray00. Blue arrows indicate the direction of flow from each source box to the target box.